On Developing and Verifying Design Abstractions for Reliable Concurrent Programming in Ada

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Abstract

Ada 95 is an expressive concurrent programming language, which allows building large multi-tasking applications. Much of the complexity of these applications stems from the interactions between the tasks. Design abstractions (such as atomic actions, conversations etc.) have been proposed to deal with such complexity. This paper argues that Petri nets offer a promising, tool-supported, technique for checking the logical correctness of abstractions. The paper illustrates the effectiveness of this approach by showing the correctness of an Ada implementation of the atomic action protocol using a variety of Petri net tools.

1. Introduction

As high-integrity systems become more sophisticated, the resulting complexity is easier to manage if the applications are represented as concurrent processes rather than sequential ones. Inevitably, the introduction of concurrency brings problems of process interaction and coordination. In trying to solve these problems, language and operating system researchers have introduced new high-level programming constructs. These design abstractions are often closely related to the specific domain being addressed. For example, software fault-tolerance has adopted the notion of conversations [16] and atomic actions [7] to facilitate the safe and reliable communication between group of processes in the presence of hardware and software failures, in addition to providing a structuring technique for such systems. Research languages such as Concurrent Pascal have been used as the basis for experimentation [12], or a set of procedural extensions or object extensions have been produced. For example, Arjuna uses the latter approach to provide a transaction-based toolkit for C++ [22]. However, it is now accepted that the procedural and object extensions are unable to cope with all the subtleties involved in synchronisation and co-operation between several communicating concurrent processes.

The main disadvantage of domain-specific abstractions is that they seldom make the transition into general-purpose programming languages or operating systems. For example, no mainstream language or operating systems supports the notion of a conversation [4]. The result is that all the hard-earned research experience is not promulgated into industrial use.

If high-level support is not going to be found in mainstream languages, the required functionality must be programmed with lower-level primitives that are available. For some years now we have been exploring the use of the Ada programming language as a vehicle for implementing reliable concurrent systems [24]. Ada 95 defines a number of expressive concurrency features [1]. Used together they represent a powerful toolkit for building higher-level protocols/design abstractions that have wide application. E.g., [24] showed how Ada 95 can be used to implement Atomic Actions. As such an abstraction is not directly available in any current programming language, this represents a significant step in moving these notions into general use. An examination of this, and other applications, shows that a number of language features are used in tandem to achieve the required result, namely: tasks, Asynchronous Transfer of Control (ATC), protected types, requeue, exceptions and controlled types.

The expressive power of the Ada 95 concurrency fea-
tures is therefore clear. What is not as straightforward is how to be confident that the higher-level abstractions produced are indeed correct. As a number of interactions are asynchronous this presents a significant verification problem. The idea of verification using Model Checking with a finite state model (FSM) of an Ada program was first presented in [5]. This method constructed a set of FSMs of individual tasks interacting via channels, and applied analysis of the interleaving semantics of the product of FSMs using tool Uppaal (whose underlying formalism is that of CCS). In this paper, we investigate a complementary approach based on Petri nets and their power to model causality between elementary events or actions directly. This can be advantageous due to the asynchronous nature of interactions between tasks. Petri nets, both ordinary [17] and high level (e.g. coloured nets [11]) offer a wide range of analysis tools to model and verify the logical correctness according to two crucial kinds of properties: (i) safety - an incorrect state cannot be entered (from any legal initial state of the system); and (ii) liveness - a desirable state will be entered (from all legal initial states of the system).

Petri nets have generally been applied to the verification of Ada programs, e.g. [21, 15]. This work has mostly been focused on the syntactic extraction of Petri nets from Ada code in such a way that the verification of properties, such as deadlock detection, could be done more efficiently. To alleviate state space explosion techniques like structural reduction [21] and decomposition [15] of ‘Ada nets’ have been proposed.

Our research aims at developing a set of design abstractions for reliable concurrent programming in Ada, and at applying Petri nets to model and verify them, using available tools, such as PEP and Design/CPN [6]. Furthermore, we distinguish between these abstractions and application code which uses them. However, we propose to deal with the unavoidable complexity of the resulting programs within a compositional approach employing a versatile library of design abstractions with well understood and formally verified properties (confidence in the abstraction can be significantly increased and the development activity itself supported by modelling, simulation and analysis of the dynamic behaviour of the Petri net model; the behaviour can be analysed either by exploring the set of reachable states of the net or its partial order semantics, such as the unfolding prefix). The latter can then be used to tackle the verification of complex designs. Thus, while we are ultimately interested in efficient model checking too, the main focus of this paper is on the semantic modelling of salient task interaction mechanisms from Ada 95. To the best of our knowledge, there has been no attempt of using Petri nets to analyse Ada 95 models of Atomic Actions, particularly with ATC and exceptions. However, some work on analysing Ada 95 programs (with ATC, protected objects, and requeue statement) with Petri nets has been recently reported in [10].

2. An Introduction to Model Checking using Petri Nets

Model checking is a technique in which the verification of a system is carried out using a finite representation of its state space. Basic properties, such as absence of deadlock or satisfaction of a state invariant (e.g. mutual exclusion), can be verified by checking individual states. More subtle properties, such as guarantee of progress, require checking for specific cycles in a graph representing the states and possible transitions between them. Properties to be checked are typically described by formulae in a branching time or linear time temporal logic [9].

The main drawback of model checking is that it suffers from the combinatorial explosion problem. That is, even a relatively small system specification may (and often does) yield a very large state space which despite being finite requires computational power for its management beyond the effective capability of available computers. To help cope with the state explosion problem a number of techniques have been proposed which can roughly be classified as aiming at implicit compact representation of the full state space of a reactive concurrent system, or at an explicit representation of a reduced (yet sufficient for a given verification task) state space of the system. Techniques aimed at reduced representation of state spaces are typically based on the independence (commutativity) of some actions, which is a characteristic feature of reactive concurrent systems, often relying on the partial order view of concurrent computation. Model checking is a technique that requires tool support. For Petri nets, there are many tools of different maturity available. These tools are categorised according to many parameters [25]. In our study, we use three relatively mature tools. One is PEP [2], which uses ordinary Place/Transition (P/T) nets and a number of model checking methods, such as reachability analysis and unfolding prefix. The second one is INA (Integrated Net Analyzer) [18]. The third is Design/CPN [26], which is based on the Coloured Petri nets and has extensive facilities for simulation and occurrence (reachability) graph analysis.

3. Developing Design Abstractions

In this section we outline abstractions which are important for reliable concurrent programming in Ada and our reasons for choosing them. Our understanding has been built on analysing the existing schemes supporting some abstractions. We believe that it is vital to develop a systematic approach for choosing and developing such abstrac-
tions. In particular, this choice has to be driven by our ability to check the abstractions (this might depend on the Ada features used to develop them). Other important issues to address are expressing general abstraction properties in the way in which they can be formally checked and developing guides for applying the abstractions correctly. Our first experiments with modelling and checking the atomic action abstraction have contributed to better understanding of these topics [6]. This section discusses some preliminary results of the on-going research.

Abstraction Overview There has been a considerable body of research on developing reliable abstractions in Ada (see, for example, [24, 19, 13]. The first stage of our research builds on analyses of several existing schemes as possible candidates for formalisation and model checking. These include conversation schemes of different types (e.g. concurrent recovery blocks), different atomic action schemes (with backward or forward error recovery, action nesting, detecting the entry and the exit deserters, with or without entry synchronisation), N-version programming with concurrent version execution [20], replicated systems [23], etc.

The following step is to come up with a systematic approach for developing such abstractions. The intention here is to propose a range of schemes which work under different fault assumptions (including software design faults, environmental faults, transient errors, exceptions raised by the underlying support, hardware faults) and which are suitable for designing concurrent systems and applications of different types.

Our further intention is to propose a set of basic abstractions which are useful for developing both reliable decentralised applications and decentralised controls for the above-mentioned reliable abstractions such as atomic actions and replicated systems [23]. These can include some of the following mechanisms: message ordering, broadcast protocols, agreement protocols, group membership support.

Another important avenue to explore here is the development of new abstractions suitable for designing real-time Ada systems (e.g. periodic and sporadic tasks, scheduling) and extending the abstractions discussed above for dealing with time concerns (e.g. by including time constraints).

Principles. In this research we are following several general principles:
- making the schemes implementing the design abstractions as reusable and as general as possible;
- relying on basic building blocks which can be used for designing several abstractions (for example, features detecting deserter processes are common for conversations, atomic actions and N-version programming);
- separating the re-usable code from the application code as much as possible.

Special measures have to be taken to make modelling and checking simpler. This can be done by applying an evolutionary development with developing the right abstractions in the right order. For example, paper [13] presents a basic distributed atomic action scheme and its several extensions, which, we believe, are much easier to model and to check when the basic scheme is checked. Model checking can be facilitated by applying several architectural solutions:
- composability: developing several simple basic abstractions and demonstrating how more complex ones can be composed;
- separating concerns: developing abstractions which are concerned with different orthogonal properties related to reliability (and dependability in general). This can make the construction of more complex abstractions and their verification simpler;
- layering: building new abstractions on the top of existing and verified ones which are designed as the underlying service layers.

Another important issue to be taken into account is developing a better understanding of how different Ada constructs can complicate the checking and cause state explosion. This will allow us to program abstractions which are easier to check. One of the solutions could be to define an Ada subset (although it is clear for us that the Ravenscar subset [3] is too simple for our needs). Furthermore, composability might make it difficult to subset. For example, nested ATC blocks add complexity but it is unlikely that we would want to disallow them.

Some of these principles as well as the intentions outlined in the previous subsection are contradictory and finding a right balance is a very important issue in this research.

Abstraction Properties. To ensure the correctness of an abstraction we should be able to formulate and check a complete set of its properties (generally speaking, it might be reasonable to develop a necessary and sufficient set of properties). To avoid any ad hoc approaches we should rely on rigorous definitions of such abstractions. Their properties are usually defined in terms of system design and use to help programmers rather than formally. For example, hardware fault tolerance schemes often rely on the fact that data saved in a stable storage can be recovered after a crash. To verify the ability of the system to be recovered we should be able to model and to check that such schemes always save data sufficient for the restart and for guaranteeing the continuous service. Other examples of such properties are: atomicity and isolation of atomic actions (absence of information exchange with the outside world), absence of the deserter processes in conversations, all-or-nothing effect of actions, mutual exclusion of the access to shared, correct action nesting. These properties should be modelled in a formal way suitable for checking the correctness of the ab-
stractions. Developing systematic approaches for describing and formalising a complete set of properties for each abstraction is the only general way for ensuring their correctness.

Guides. After the correctness of the design abstractions has been demonstrated, the programmers can apply them for system design. Unfortunately this can be an error prone process because it is not supported by the compiler or runtime checks and because the correctness of the design abstractions does not mean that they are always applied correctly. Although some schemes may perform several runtime checks, it is usually not practical to develop and to use schemes which are able to detect and tolerate all possible types of misuse. This is why all schemes supporting design abstractions of interest assume that there are some rules and restrictions on using them and that the programmers follow them. This shows the importance of developing guides explaining how to apply design abstractions implemented as the concrete Ada schemes. These guides will include templates and a set of conventions for programmers. They are to be prepared by system or fault tolerance programmers. For example, it is nearly impossible to prove or to guarantee in the runtime that a set of Ada tasks inside an atomic action do not exchange information with the outside world. The only practical solution is to describe how this can be achieved in a programmers’ guide.

It might be possible to develop supplementary tools to work with the designed Ada code (maybe with some annotations included as comments) and check that such rules have been followed while applying the design abstractions. In this case the guides can be applied together with such tools.

4. Model of Simple Atomic Actions

Atomic Actions. An atomic action is a dynamic mechanism for controlling the joint execution of a group of tasks such that their combined operation appears as an indivisible actions. Essentially, an action is atomic if the tasks performing it can detect no state change except those performed by themselves, and if they do not reveal their state changes until the action is complete. Atomic actions can be extended to include forward or backward error recovery. In this section we will focus only on forward error recovery using exception handling [7]. If an exception occurs in one of the tasks active in an atomic action then that exception is raised in all tasks active in the action. The exception is said to be asynchronous as it originates from another task.

Atomic Actions in Ada. To show how atomic actions can be programmed in Ada [24], consider a simple non-nested action between, say, three tasks. The action is encapsulated in a package with three visible procedures, each of which is called by the appropriate task. It is assumed that no tasks are aborted and that there are no deserter tasks [12].

```ada
package simple_action is
  procedure T1(params : param);
    -- from Task 1
  procedure T2(params : param);
    -- from Task 2
  procedure T3(params : param);
    -- from Task 3
end simple_action;
```

The body of the package automatically provides a well-defined boundary, so all that is required is to provide the indivisibility. A protected object, Controller, can be used for this purpose. The package’s visible procedures call the appropriate entries and procedures in the protected object. The body of the package is given below.

```ada
with Ada.Exceptions; use Ada.Exceptions;
package body action is
  type Vote_T is (Commit, Aborted);
  protected controller is
    entry Wait_Abort(E: out Exception_Id);
    entry Done;
    entry Cleanup (Vote : Vote_t;
      Result : out Vote_t);
    procedure Signal_Abort(E: Exception_Id);
  private
    entry Wait_Cleanup(Vote : Vote_t;
      Result : out Vote_t);
    Killed : boolean := False;
    Releasing_cleanup : Boolean := False;
    Releasing_Done : Boolean := False;
    Reason : Exception_Id;
    Final_Result : Vote_t := Commit;
    informed : integer := 0;
  end controller;
  -- any local protected objects for
  -- communication between actions
  protected body controller is
    entry Wait_Abort(E: out Exception_id)
      when killed is
      begin
        E := Reason;
        informed := informed + 1;
        if informed = 3 then
          Killed := False;
          informed := 0;
        end if;
      end Wait_Abort;
    entry Done when Done'Count = 3 or
      Releasing_Done is
      begin
        if Done'Count > 0 then
          Releasing_Done := True;
        else
          Final_Result := Commit;
        end if;
      end Done;
end simple_action;
```

```ada
with Ada.Exceptions; use Ada.Exceptions;
package body action is
  type Vote_T is (Commit, Aborted);
  protected controller is
    entry Wait_Abort(E: out Exception_Id);
    entry Done;
    entry Cleanup (Vote : Vote_t;
      Result : out Vote_t);
    procedure Signal_Abort(E: Exception_Id);
  private
    entry Wait_Cleanup(Vote : Vote_t;
      Result : out Vote_t);
    Killed : boolean := False;
    Releasing_cleanup : Boolean := False;
    Releasing_Done : Boolean := False;
    Reason : Exception_Id;
    Final_Result : Vote_t := Commit;
    informed : integer := 0;
  end controller;
  -- any local protected objects for
  -- communication between actions
  protected body controller is
    entry Wait_Abort(E: out Exception_id)
      when killed is
      begin
        E := Reason;
        informed := informed + 1;
        if informed = 3 then
          Killed := False;
          informed := 0;
        end if;
      end Wait_Abort;
    entry Done when Done'Count = 3 or
      Releasing_Done is
      begin
        if Done'Count > 0 then
          Releasing_Done := True;
        else
          Final_Result := Commit;
        end if;
      end Done;
end simple_action;
```
Each component of the action \((T1, T2, \text{and } T3)\) has identical structure. The component executes a select statement with an abortable part. The triggering event is signalled by the controller protected object if any component indicates that an exception has been raised and not handled locally in one of the components. The abortable part contains the actual code of the component. If this code executes without incident, the controller is informed that this component is ready to commit the action.

**Place/Transition net model.** We now consider Petri nets for this Ada code. Each of the client tasks will have an identical PN, specialised only in its labelling of transitions and places. The controller will also be modelled as a single Petri net. Our graphical support for capturing the Petri nets is a Petri net editor PED [14], which allows hierarchical and fragmentated construction of P/T nets, and their export to an extensive range of formats including those accepted by analysis tools like PEP and INA. Figure 1 presents the task model (a) and the controller model (b).

Places and transitions which are not shaded, such as start1 and arr1 are individual for the task net (here we show the net for Task 1). Those places and transitions which are shaded are so called (in PED) logical places and transitions – they are used to interconnect subnets to form larger nets. In other words, by declaring places or transitions in different subnets as logical in PED, we virtually merge such places and transitions in the overall net provided that they have the same label, e.g. waitAbort and sigAbort1. Note that the net models use the so called test or read-only arcs (arcs with a black dot at the transition end), and weighted arcs. The former are used to show the fact that transitions in the task net can test the state of shared variable, such as Killed, which is modelled by two complementary (cannot be simultaneously marked with a token) places notKilled and Killed in the controller net.

Our basic idea of modelling the Ada code for the Atomic Action behaviour with P/T nets is as follows. We represent...
states of each task as (unshaded) places and key actions local for the task as unshaded (solid bars) transitions. Arriving in the Atomic Action by the task is represented by transition arr1. This also generates a token in the place waitAbort, which belongs to the controller and counts the number of tasks that have actually entered the Atomic Action. The place labelled comp1 corresponds to the state of the task in which the task performs normal computation. From this state the task may either: (a) execute transition done1 and go to the Local Done state of normal completion of the action (place locDone1), or (b) it may raise an exception by firing transition sigAbort1 (this corresponds to executing the SignalAbort procedure, which switches the state of the Killed flag from false to true – a token is toggled from place notKilled to Killed), or (c) it may be forced to go to the Error-Handling state (place handling1), either from the Normal Computation state or from the the Local Done state because of some task's (even itself) raising an exception, in which case transition except12 will be fired.

Subsequent action of the task depends on whether the task ends in the Local Done or in the Error-Handling state. If the former, the task provides a condition for the controller to fire a shared transition doneAll (corresponding to the execution of the Done entry by all tasks). If the task is in the Error-Handling state, it handles the exception and depending on the result of the handling it votes either for Action Commit or Action Abort.

The voting mechanism used in Atomic Actions allows one task voting for Abort to force the entire operation into Failure. In our Petri net model, this is achieved by using the following three transitions sendAbort1, sendComm1 or sendAbComm1, individual for the task. These transitions are connected to two complementary places voteNotAbort and voteAbort in the controller net. Initially, when the voting begins, a token is assumed to be placed into place voteNotAbort. While none of the tasks votes for Abort, the token remains in this place, and if the task votes for Commit (this corresponds of the handling ok flag being set in the task), transition sendComm1 fires due to the reading arc from place voteNotAbort. As soon as one of the tasks votes for Abort the token is switched from it fires transition sendAbort1, which toggles the token from voteNotAbort to voteAbort in the controller. This corresponds to assigning the state of the global flag Final result to aborted in the Cleanup entry. After that, in all tasks, regardless of their individual voting, transition sendAbComm1 will fire due to the reading arc from place voteAbort.

The voting is complete when the task is in the state where it is ready to check the value of the decision flag. This corresponds to a token in the voted1 place. At this point all tasks synchronise on firing shared transitions commitAll or abortAll, which are respectively preconditioned by the controller’s places voteNotAbort and voteAbort. If the former
fires it puts a token in the local success1 place, if the latter the local fail1 is marked. After that the task fires one of the two possible restart transitions which corresponds to bringing the task to the state where it is ready to execute the Atomic Action again.

Using the PED tool we constructed the model of the system from the task and controller fragments. Once the appropriate places and transitions are merged the actual behavioural interaction between task and controller is achieved through the following two main mechanisms: (i) synchronisation on shared transitions, which is similar to rendez-vous (blocking) synchronisation, and (ii) communication via shared places, which is similar to asynchronous (non-blocking) communication.

**Verification.** This P/T net model of the Ada code can be exported from PED to analysis tools, such as INA or PEP. We used PEP, in which we could simulate the token game and perform reachability analysis to verify by Model Checking the key properties of the algorithm. First, if “Task1” is in place success1 then it must not be possible for any of the other tasks (say 2) to be in fail2. This is presented to the reachability analysis tool by the following logic statement: success1, fail2. This test gives the <NO> result, i.e. such a marking in which these two places are marked is not reachable.

Similarly, to the test for reachability of a marking in which both tasks end in success state: success1, success2. The tool reacts with <YES> and produces:

SEQUENCE: arr2, done2, arr1, done1, doneAll

which is a sequence leading to the global success state.

When setting the option Calculate all paths to true, the tool produces the following list of firing sequences:

SEQUENCE:
arr2, done2, arr1, done1, doneAll
arr2, arr1, done2, done1, doneAll
arr1, arr2, done2, done1, doneAll
arr1, done1, arr2, done2, doneAll
arr2, arr1, done1, done2, doneAll
arr1, arr2, done1, done2, doneAll

This set, however, includes only those paths which go through the locDone states, but not those which are the result of successful handling and overall Commit voting. This is caused by the fact the system searches for all paths satisfying the shortest length criterion.

The effect of a coherent error handling can be tested by: fail1, fail2. This results in:

SEQUENCE: arr1, done1, arr2, sigAbort2, except21, sendAbort2, except12, sendAbComm1, sync, abortAll
arr2, arr1, done1, sigAbort2, except21, sendAbort2, except12, sendAbComm1, sync, abortAll

all together over 600 paths. These assertions imply inconsistency is not possible.

We have also used tool INA to verify the various behavioural (safety and liveness) properties. The results of this analysis are:

Safety Properties:
Safe – No
Bounded – Yes
Dead State Reachable – No
Covered by Transition-Invariants – Yes
Resettable, reversible (to home state) – Yes
Dead transitions exist – No
Live – Yes
Live and Safe – No

The computed reachability graph has 76 states.

The INA tool allows to state properties in the form of CTL (Computational Tree Logic) [8] formulas. We can formulate properties of interest, such as whether there exists a path which leads to a state where one task ends in success while the other in fail:

EF((P18 & P21) V (P19 & P20))

Here P18 (P19) stands for success1 (success2) and P21 (P20) for fail2 (fail1). The result of the check is:

sat EF((P18 & P21) V (P19 & P20)): FALSE

Our preliminary results on using Colored Petri nets for modelling and analysing the Atomic Action scheme were recently reported in [6].

5. Conclusion

This paper is only a preliminary attempt in pursuing our chosen direction of research, in which we would like to develop a more comprehensive methodology for verifying high-integrity systems built of Atomic Actions and implemented in Ada 95.

The major new aspects of this work, which also reveal the potentially exploitable advantages of the Petri net approach over the State Machine one [5], are: (i) refinement of both states and transitions; (ii) analysis of behaviour at the true concurrency and causality level; (iii) high-level aspects of modelling, such as parametrisation, are possible using high-level Petri nets.

For example, if refinement with threads (e.g., task spawning), recursive atomic actions, etc. were possible in the modelled systems, then Petri nets would provide a much more efficient way of modelling than state machines. We have only shown the way of modelling interaction mechanisms at the semantical level. Part of the intended future
work would be to adopt the existing or develop new methods of extracting Petri nets from the Ada 95 syntax.

We have outlined the important aspect of the development of new design abstractions, which must be more amenable to model checking in general and use of Petri nets in particular. We have also identified a way for formulating properties of these abstractions to be checked, and proposed an approach to develop practical guidelines for applying the abstractions in real designs.

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References


